



JOC3150L Series

DC Input, 0.8A, Gate Driver Photo Coupler

Description

The JOC3150L series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to an integrated circuit with a power output stage in a plastic DIP8 package with different lead forming options.

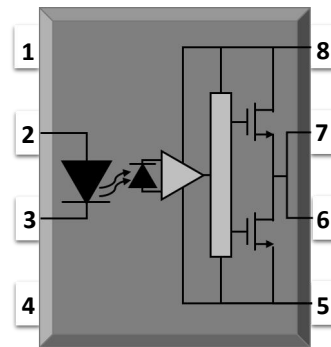
Features

- High isolation 5000 VRMS
- DC input with a high speed driver
- Operating temperature range - 40 °C to 100 °C
- REACH compliance
- MSL class 1
- Regulatory Approvals
  - UL
  - VDE

Applications

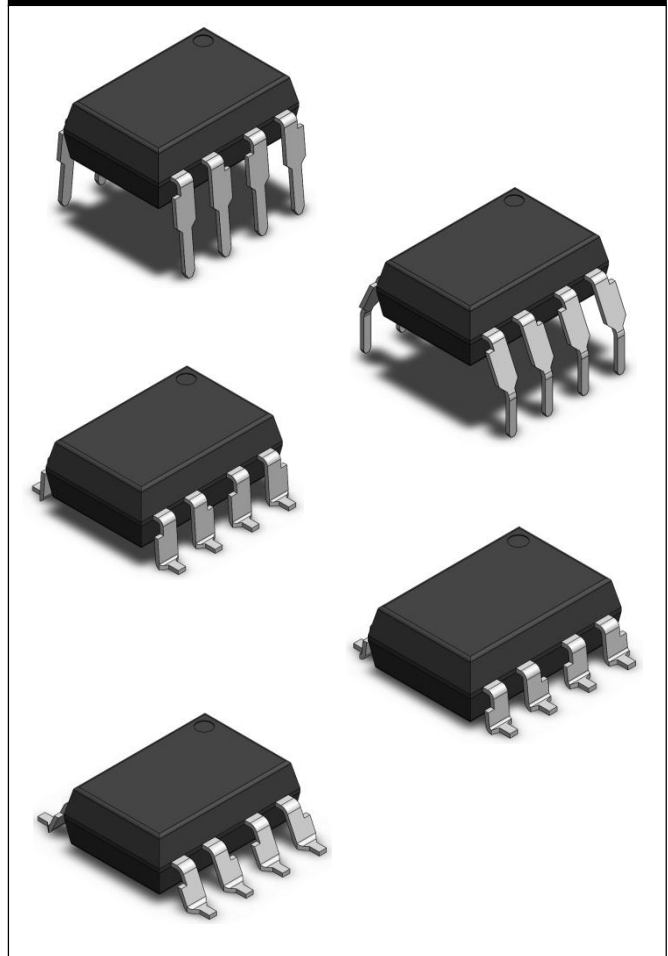
- Isolated IGBT/Power MOSFET gate drive
- Industrial Inverter
- AC brushless and DC motor drives
- Induction Heating

SCHEMATIC



PIN DEFINITION

<b>1.NC</b>	<b>8.VCC</b>
<b>2.Anode</b>	<b>7.VO</b>
<b>3.Cathode</b>	<b>6.VO</b>
<b>4.NC</b>	<b>5.GND</b>



### ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT	Note
INPUT				
Forward Current	IF	25	mA	
Peak Forward Current	IFP	50	mA	1
Peak Transient Current	IF(trans)	1	A	2
Operating Frequency	f	50	kHz	
Reverse Voltage	VR	5	V	
Input Power Dissipation	PI	100	mW	
OUTPUT				
Supply Voltage	VCC	35	V	
Output Voltage	VO	35	V	
Peak Output Current	IO	0.8	A	
Output Power Dissipation	PO	250	mW	
COMMON				
Total Power Dissipation	Ptot	295	mW	
Isolation Voltage	Viso	5000	Vrms	3
Operating Temperature	Topr	-40~100	°C	
Storage Temperature	Tstg	-55~150	°C	
Soldering Temperature	Tsol	260	°C	4

Note 1. 50% duty, 1ms P.W

Note 2.  $\leq 1\mu\text{s}$  P.W, 300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

Note 4. For 10 seconds

### TRUTH TABLE

LED	VDD-VSS "Positive Going" (Turn-on)	VDD-VSS "Negative Going" (Turn-off)	VO
Off	0V to 30V	0V to 30V	Low
On	0V to 11.5V	0V to 10V	Low
On	11.5V to 13.5V	10V to 12V	Transition
On	13.5V to 30V	12V to 30V	High

### RECOMMENDED OPERATION CONDITIONS

PARAMETER	SYMBOL	MIN.	MAX.	UNIT
Operating Temperature	TA	-40	100	°C
Supply Voltage	VCC	10	30	V
Input Current (ON)	IF(ON)	7	16	mA
Input Voltage (OFF)	VF(OFF)	0	0.8	V

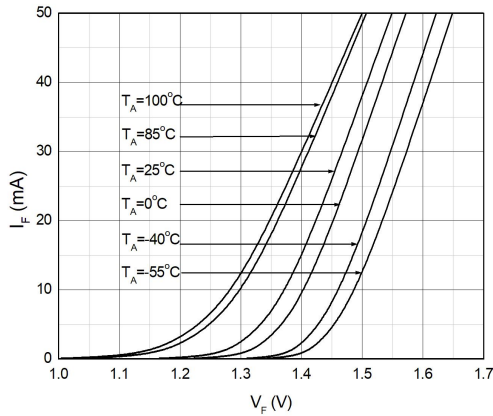
### ELECTRICAL OPTICAL CHARACTERISTICS (VCC=30V, VEE=GND, TA=25°C unless specified otherwise)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
INPUT CHARACTERISTICS							
Forward Voltage	VF	-	1.38	1.8	V	IF=10mA	
Reverse Current	IR	-	-	10	μA	VR=5V	
Input Capacitance	Cin	-	13	-	pF	V=0, f=1MHz	
OUTPUT CHARACTERISTICS							
High Level Supply Current	ICCH	-	1.9	3	mA	IF= 7mA to 10mA, VO= Open	
Low Level Supply Current	ICCL	-	2.1	3	mA	VF = 0 to 0.8V, VO= Open	
TRANSFER CHARACTERISTICS							
High Level Output Voltage	VOH	VCC-2.5	VCC-1.5	-	V	IF= 10mA, IO= -100mA	
Low Level Output Voltage	VOL	-	VEE+0.25	VEE+0.4	V	IF= 0mA, IO= 100mA	
High Level Output Current	IOPH	-0.3	-	-	A	VO= VCC-3.0V	
		-0.8	-	-	A	VO= VCC-6.0V	
Low Level Output Current	IOPL	0.3	-	-	A	VO= VEE+1.5V	
		0.8	-	-	A	VO= VEE+2.5V	
Input Threshold Current	IFLH	-	2	5	mA	IO= 0mA, VO> 5V	
Input Threshold Voltage	VFHL	0.8	-	-	V	IO= 0mA, VO< 5V	
Under Voltage Lockout Threshold	VUVLO+	6.9	7.8	8.7	V	IO= 10mA, VO> 5V	
	VUVLO-	5.9	6.7	7.5	V	IO= 10mA, VO< 5V	
Isolation Resistance	Riso	10 <sup>12</sup>	10 <sup>14</sup>	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	CIO	-	1.0	-	pF	V=0, f=1MHz	

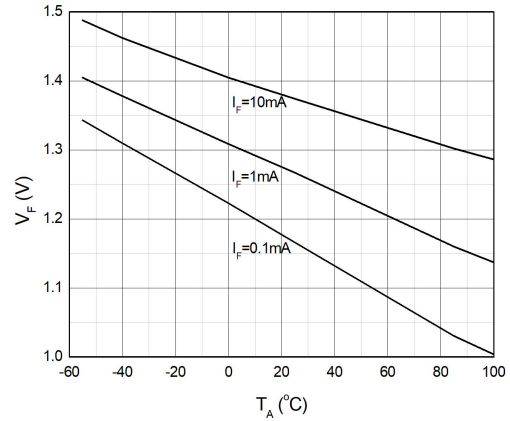
ELECTRICAL OPTICAL CHARACTERISTICS (VCC=30V, VEE=GND, TA=25°C unless specified otherwise)							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS							
Propagation Delay Time to Output Low Level	TPHL	50	250	500	ns	IF= 7 to 16mA, CL= 1nF, RL= 30Ω, f= 10kHz, Duty = 50%, TA= 25 °C	
Propagation Delay Time to Output High Level	TPLH	50	220	500	ns		
Pulse Width Distortion	TPHL-TPLH	-	30	200	ns		
Propagation Delay Skew	tPSK	-200	-	200	ns		
Rise Time	tr	-	30	-	ns		
Fall Time	tf	-	30	-	ns		
UVLO Turn On Delay	tUVLO(ON)	-	1.6	-	μs	IF= 10mA, VO> 5V	
UVLO Turn Off Delay	tUVLO(OFF)	-	0.4	-	μs	IF= 10mA, VO< 5V	
Common Mode Transient Immunity at Logic High	CMH	-20	-	-	kV/μs	IF=7 to 16mA VCC= 30V, TA= 25 °C, VCM= 2kV	
Common Mode Transient Immunity at Logic Low	CML	20	-	-	kV/μs	IF=0mA VCC= 30V, RL, TA= 25 °C, VCM= 2kV	

**CHARACTERISTIC CURVES**

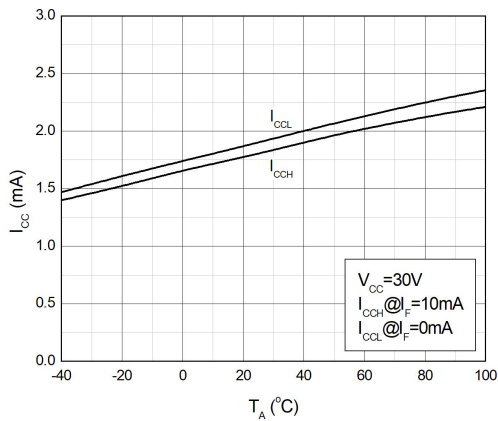
**Fig.1 Forward Current vs. Forward Voltage**



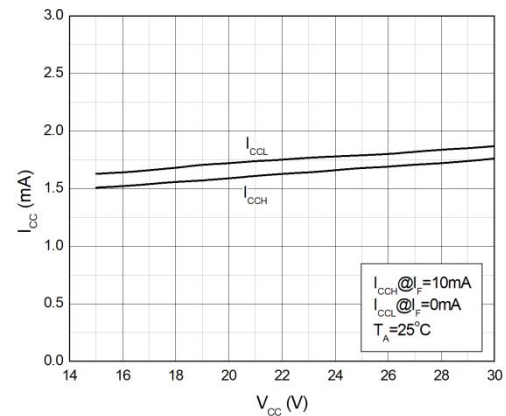
**Fig.2 Forward Voltage vs. Ambient Temperature**



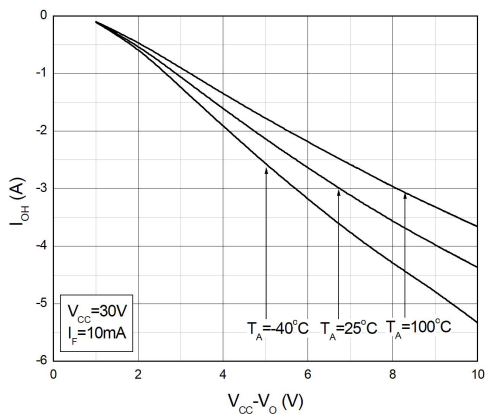
**Fig.3 Supply Current vs. Ambient Temperature**



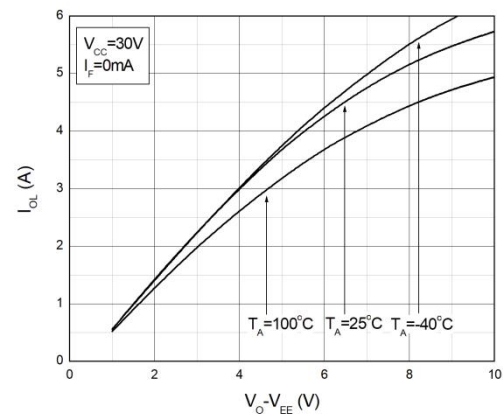
**Fig.4 Supply Current vs. Supply Voltage**



**Fig.5 High Level Output Voltage vs. High Level Output Current**

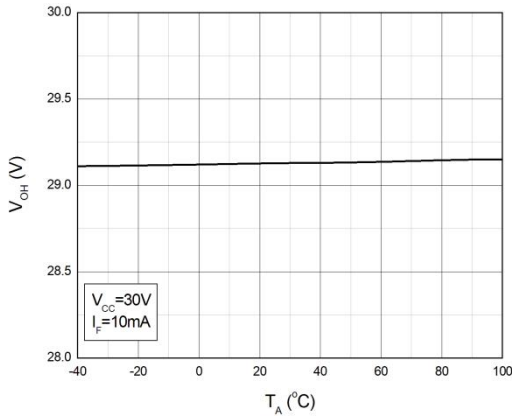


**Fig.6 Low Level Output Voltage vs. Low Level Output Current**

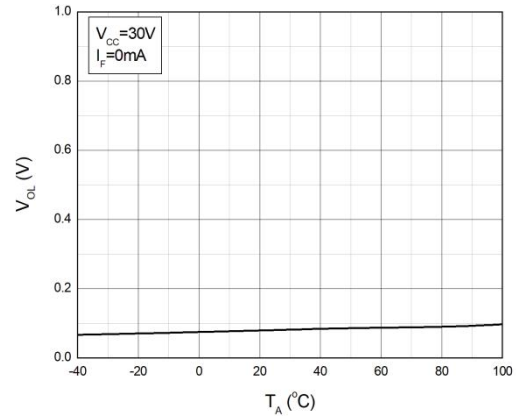


**CHARACTERISTIC CURVES**

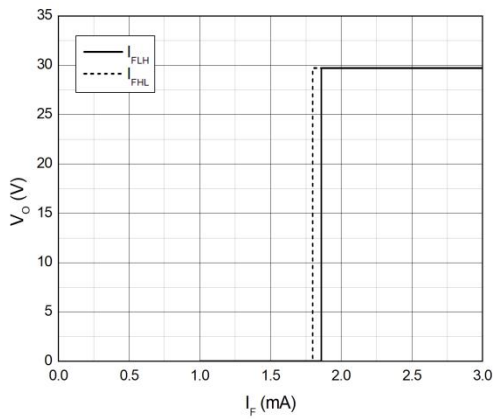
**Fig.7 High Level Output Voltage vs. Ambient Temperature**



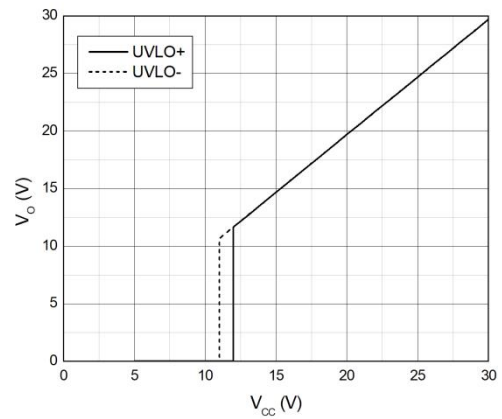
**Fig.8 Low Level Output Voltage vs. Ambient Temperature**



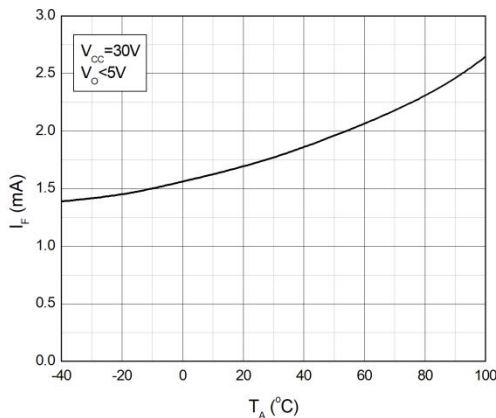
**Fig.9 Output Voltage vs. Forward Current**



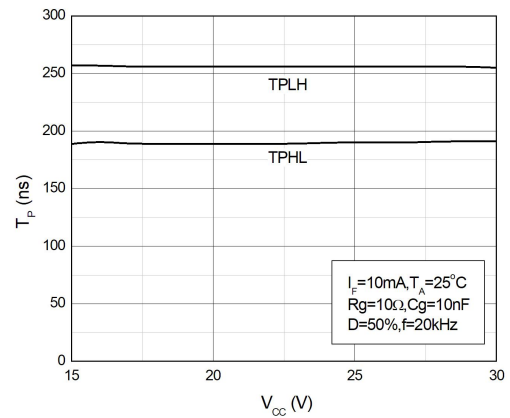
**Fig.10 Output Voltage vs. Supply Voltage**



**Fig.11 Forward Current vs. Ambient Temperature**

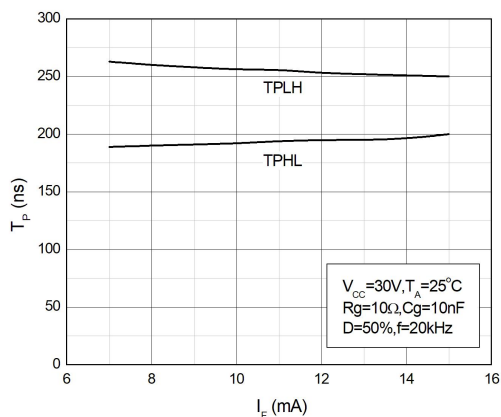


**Fig.12 Propagation Delay vs. Supply Voltage**

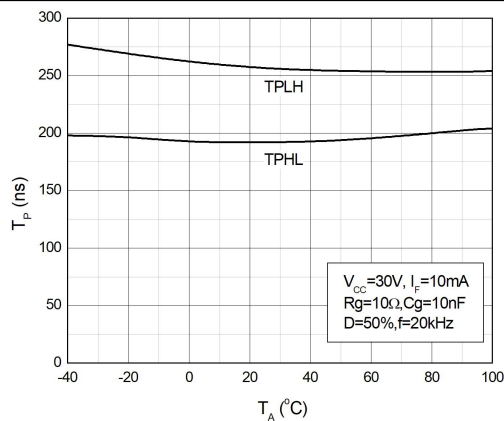


**CHARACTERISTIC CURVES**

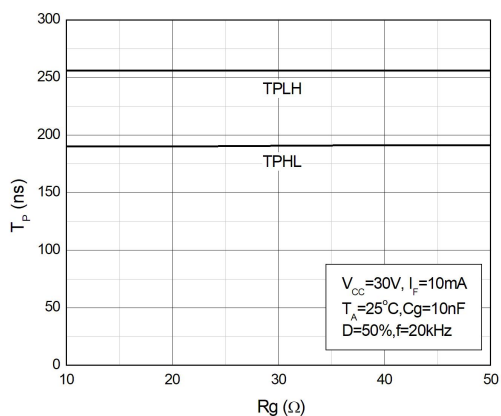
**Fig.13 Propagation Delay vs. Forward Current**



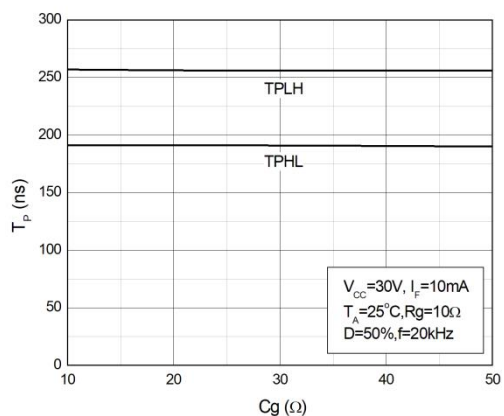
**Fig.14 Propagation Delay vs. Ambient Temperature**



**Fig.15 Propagation Delay vs. Load Resistance**

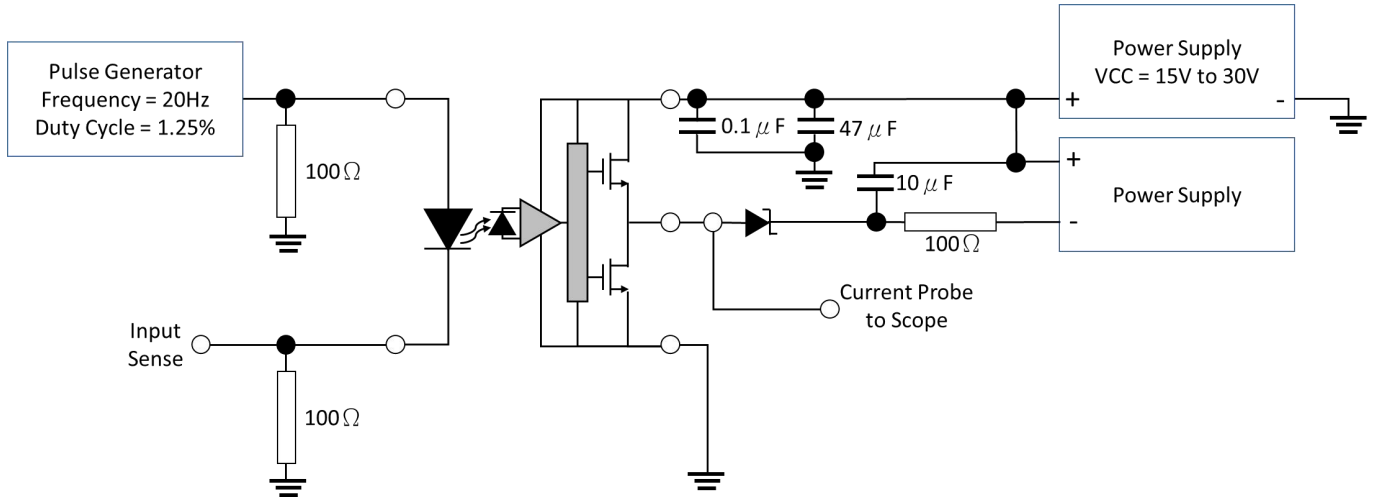


**Fig.16 Propagation Delay vs. Load Capacitance**

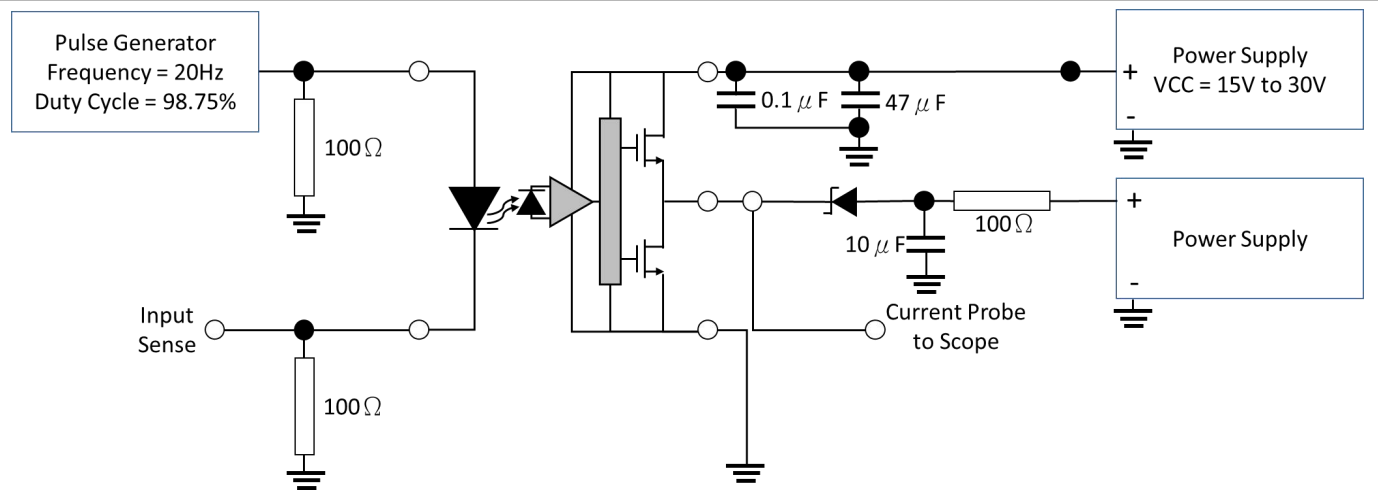


**TEST CIRCUITS**

**Fig.17 Test Circuits for IOH**



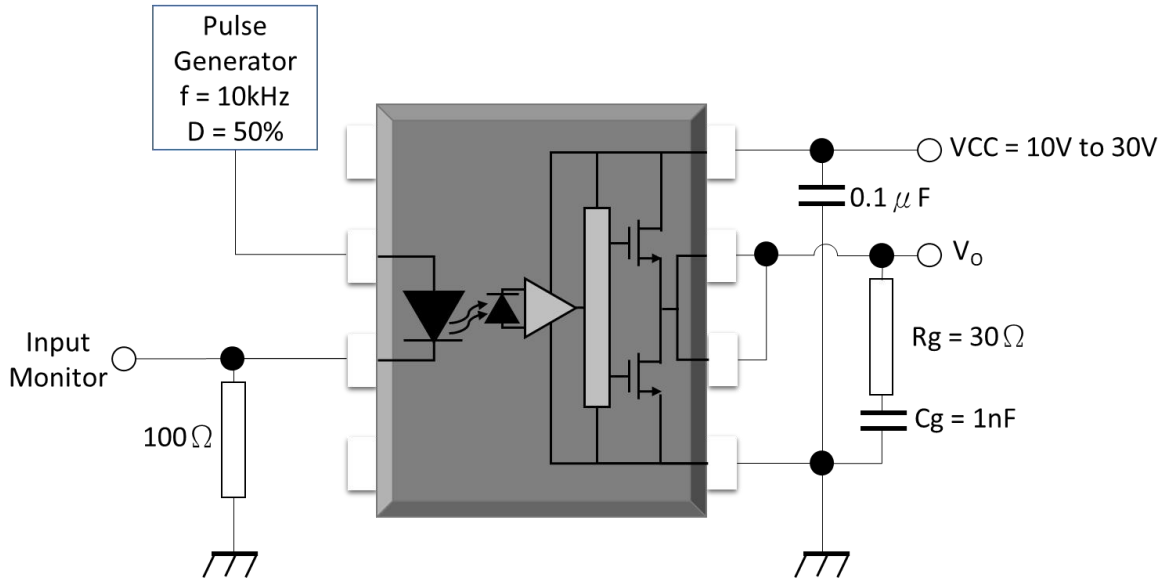
**Fig.18 Test Circuits for IOL**



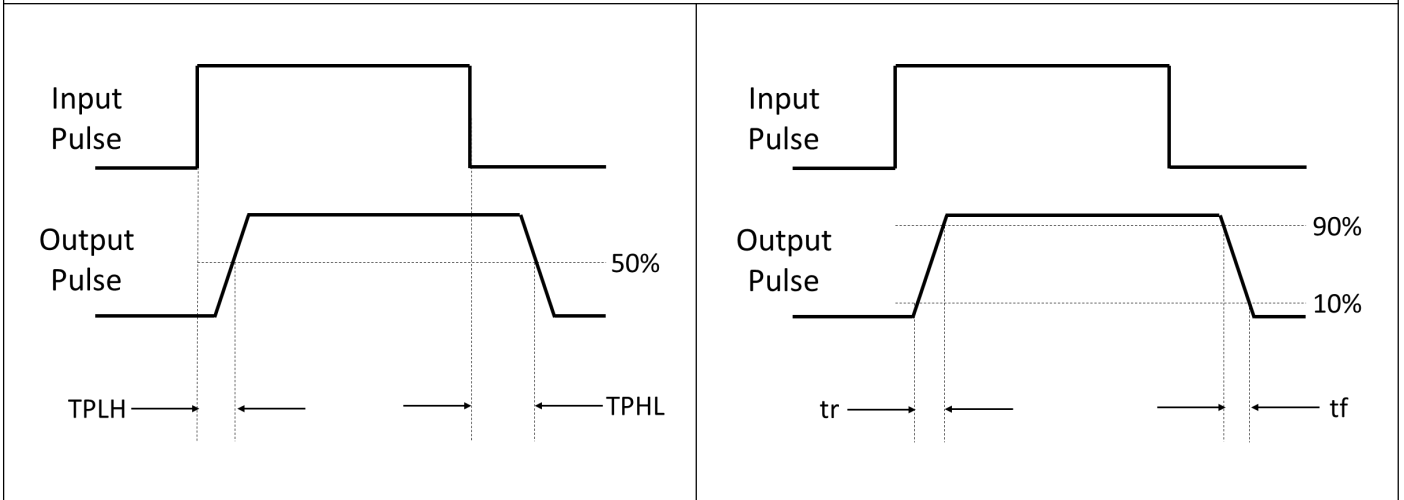


**TEST CIRCUITS**

**Fig.19 Test Circuits for TPHL, TPLH, tr, tf**

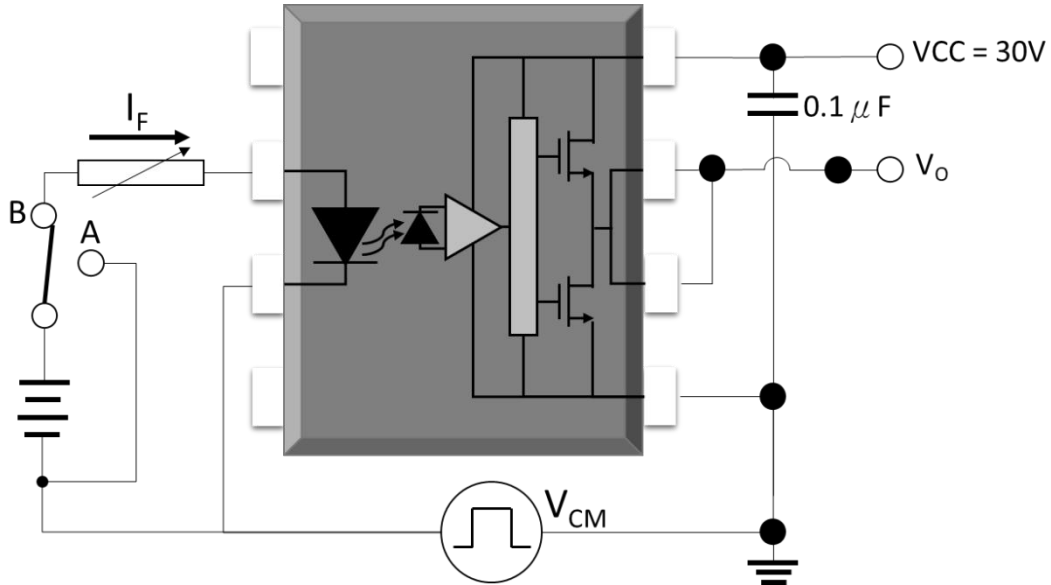


**Fig.20 Waveforms of TPHL, TPLH, tr, tf**

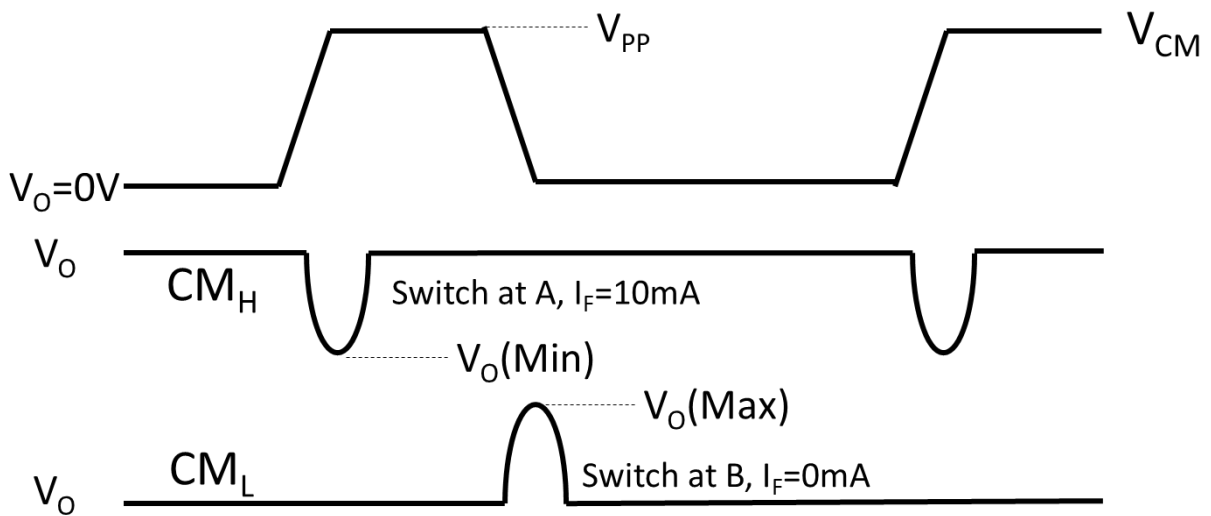


**TEST CIRCUITS**

**Fig.21 Test Circuits for Common Mode Transient Immunity**

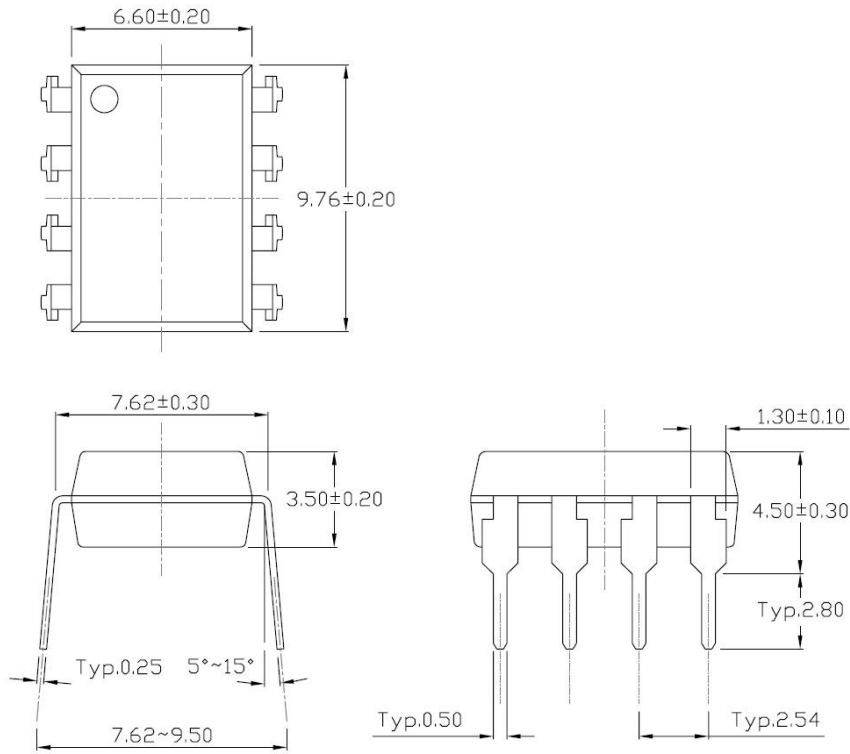


**Fig.22 Waveforms of Common Mode Transient Immunity**

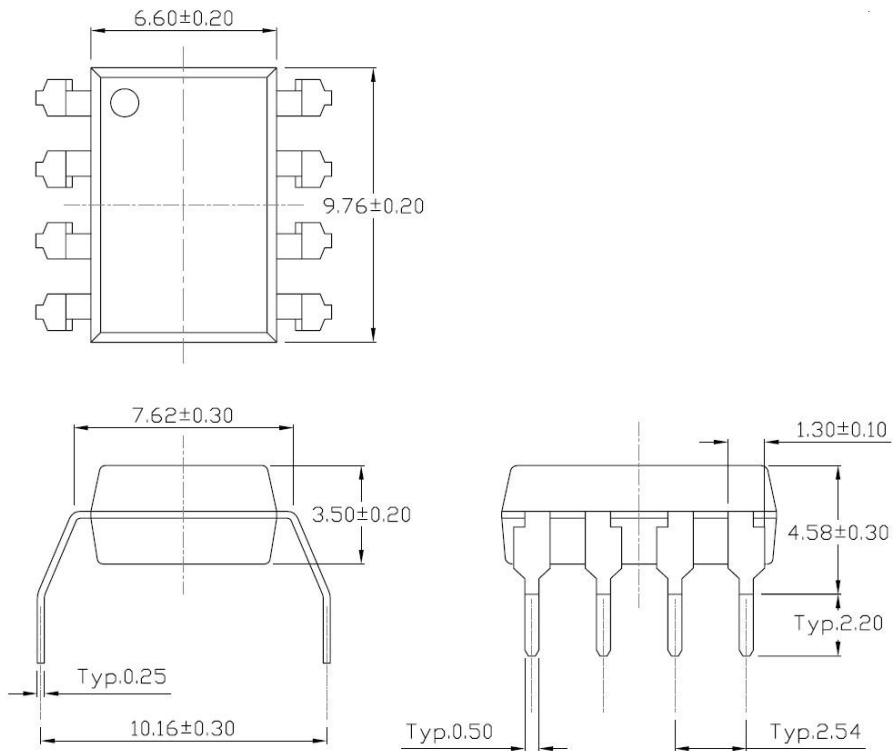


**PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)**

**Standard DIP – Through Hole (DIP Type)**

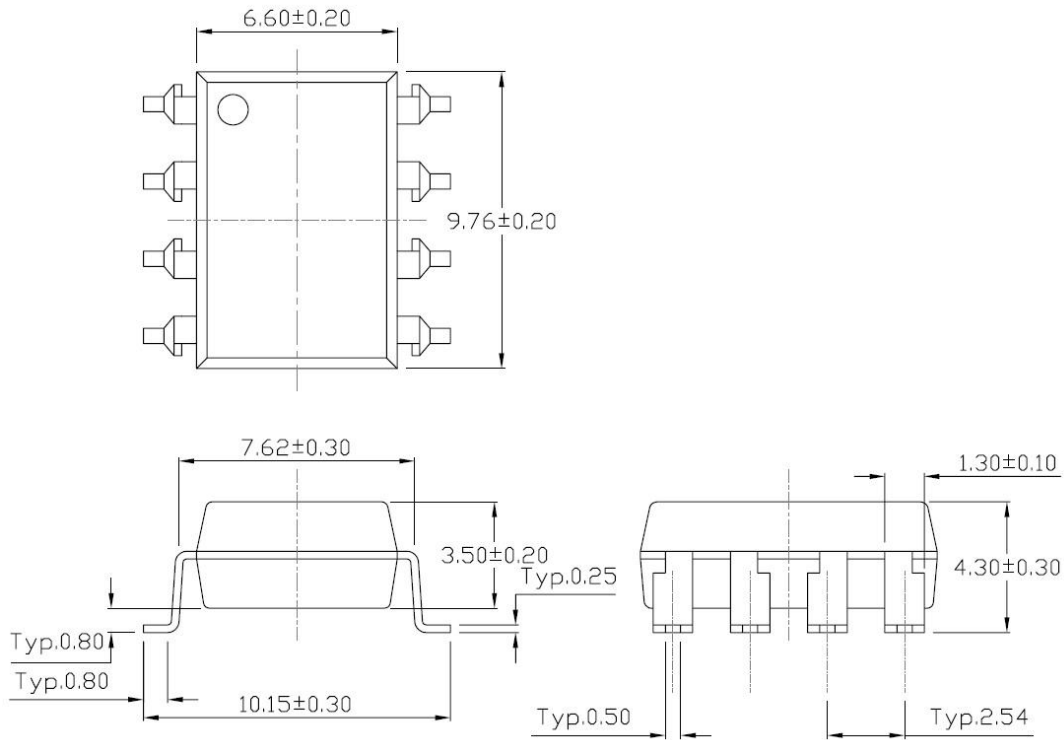


**Gullwing (400mil) Lead Forming – Through Hole (M Type)**

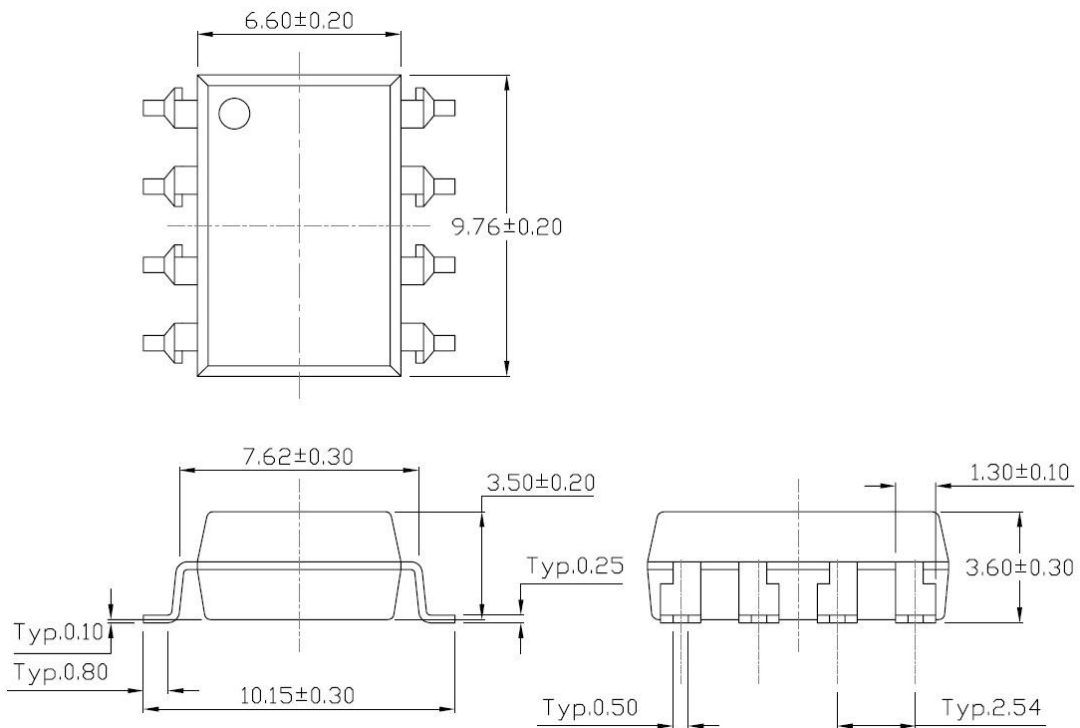


**PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)**

**Surface Mount Lead Forming (S Type)**

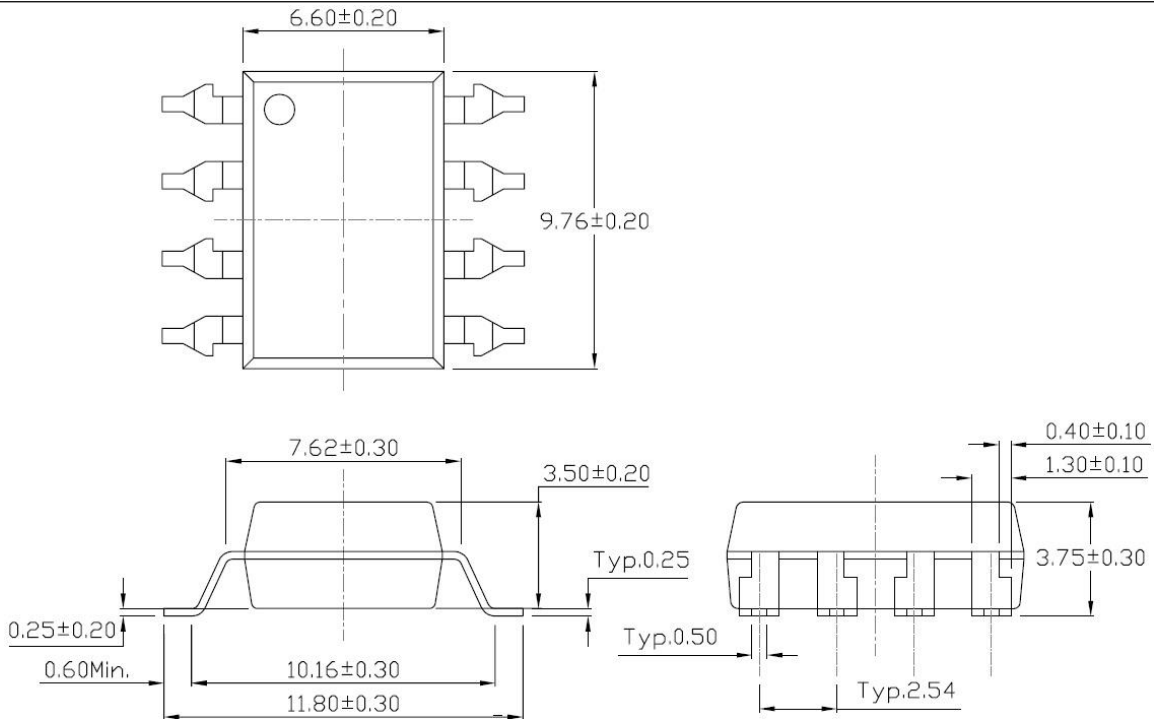


**Surface Mount (Low Profile) Lead Forming (SL Type)**



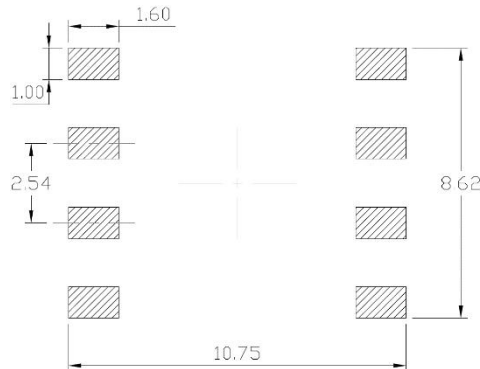
**PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)**

**Surface Mount (Gullwing) Lead Forming (SLM Type)**

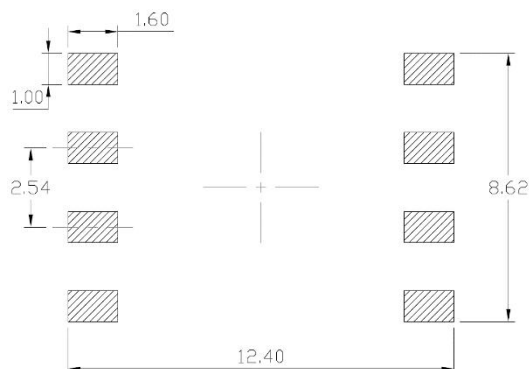


**Recommended Solder Mask (Dimensions in mm unless otherwise stated)**

**Surface Mount Lead Forming & Surface Mount (Low Profile) Lead Forming**

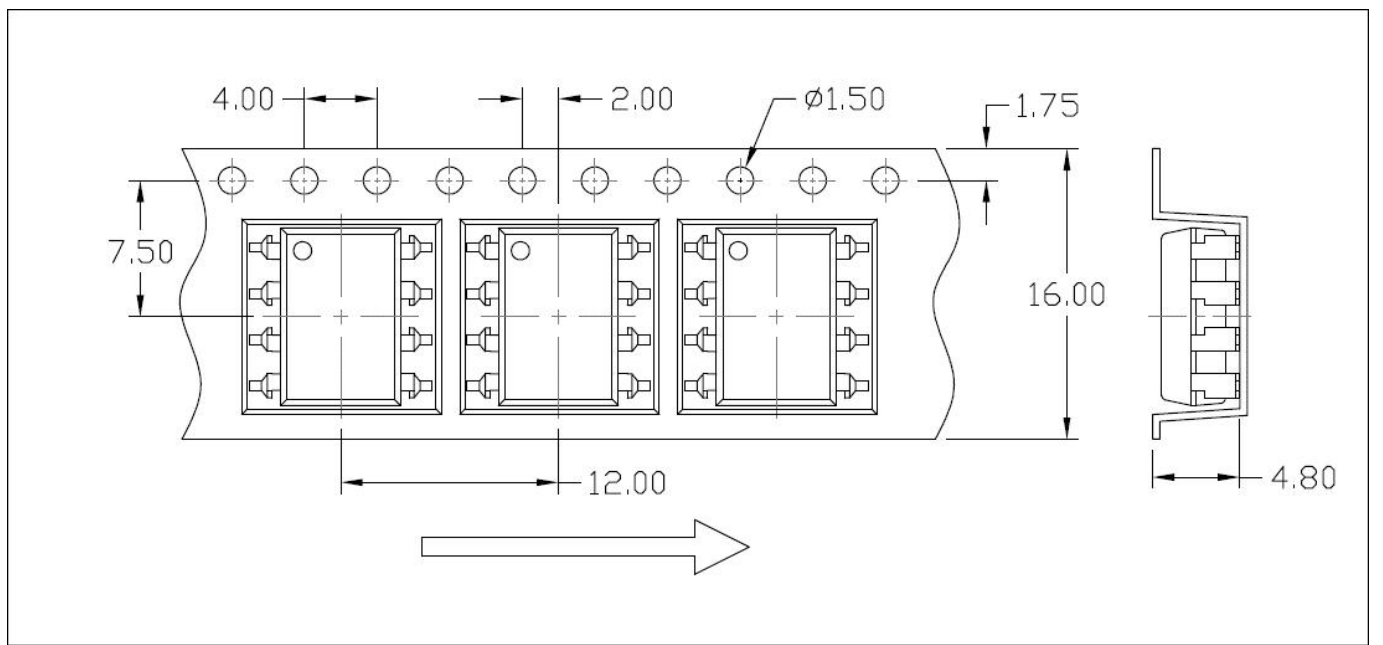


**Surface Mount (Gullwing) Lead Forming**

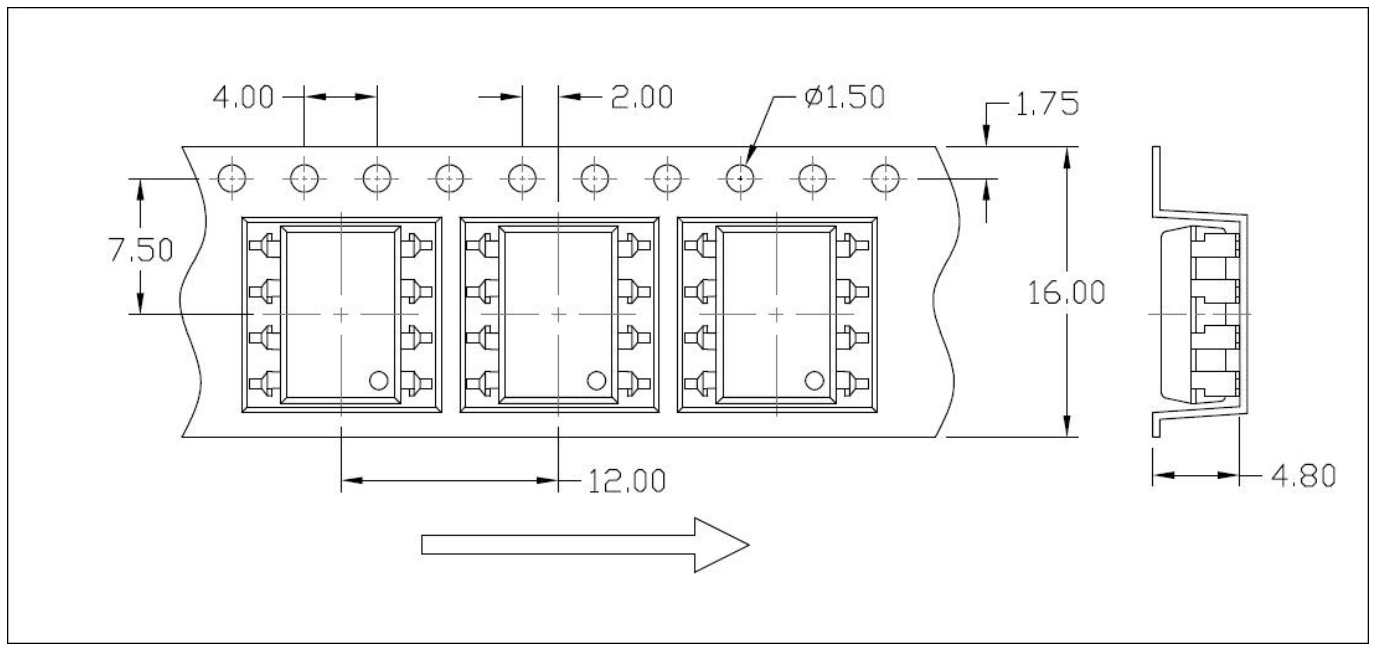


**Carrier Tape Specifications (Dimensions in mm unless otherwise stated)**

**Option S(T1) & SL(T1)**

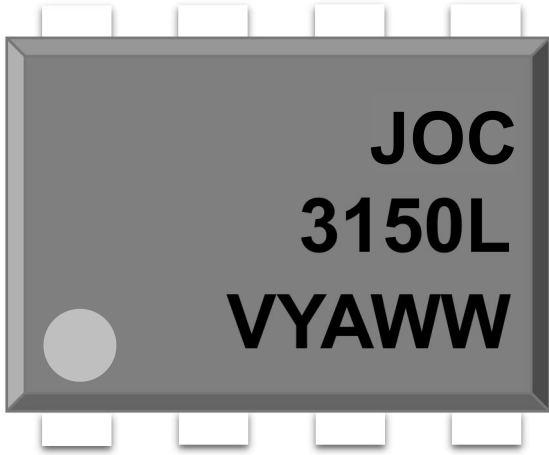


**Option S(T2) & SL(T2)**



**ORDERING AND MARKING INFORMATION**

**MARKING INFORMATION**



**JOC** : Company Abbr.  
**3150L** : Part Number  
**V** : VDE Option  
**Y** : Fiscal Year  
**A** : Manufacturing Code  
**WW** : Work Week

**ORDERING INFORMATION**

**JOC3150L(Y)(Z)-GV**

JOC – Company Abbr.  
 3150L – Part Number  
 Y – Lead Form Option  
 (M/S/SL/SLM/None)  
 Z – Tape and Reel Option (T1/T2)  
 G – Material Option  
 (G: Green, None: Non-Green)  
 V – VDE Option (V or None)

**LABEL INFORMATION**

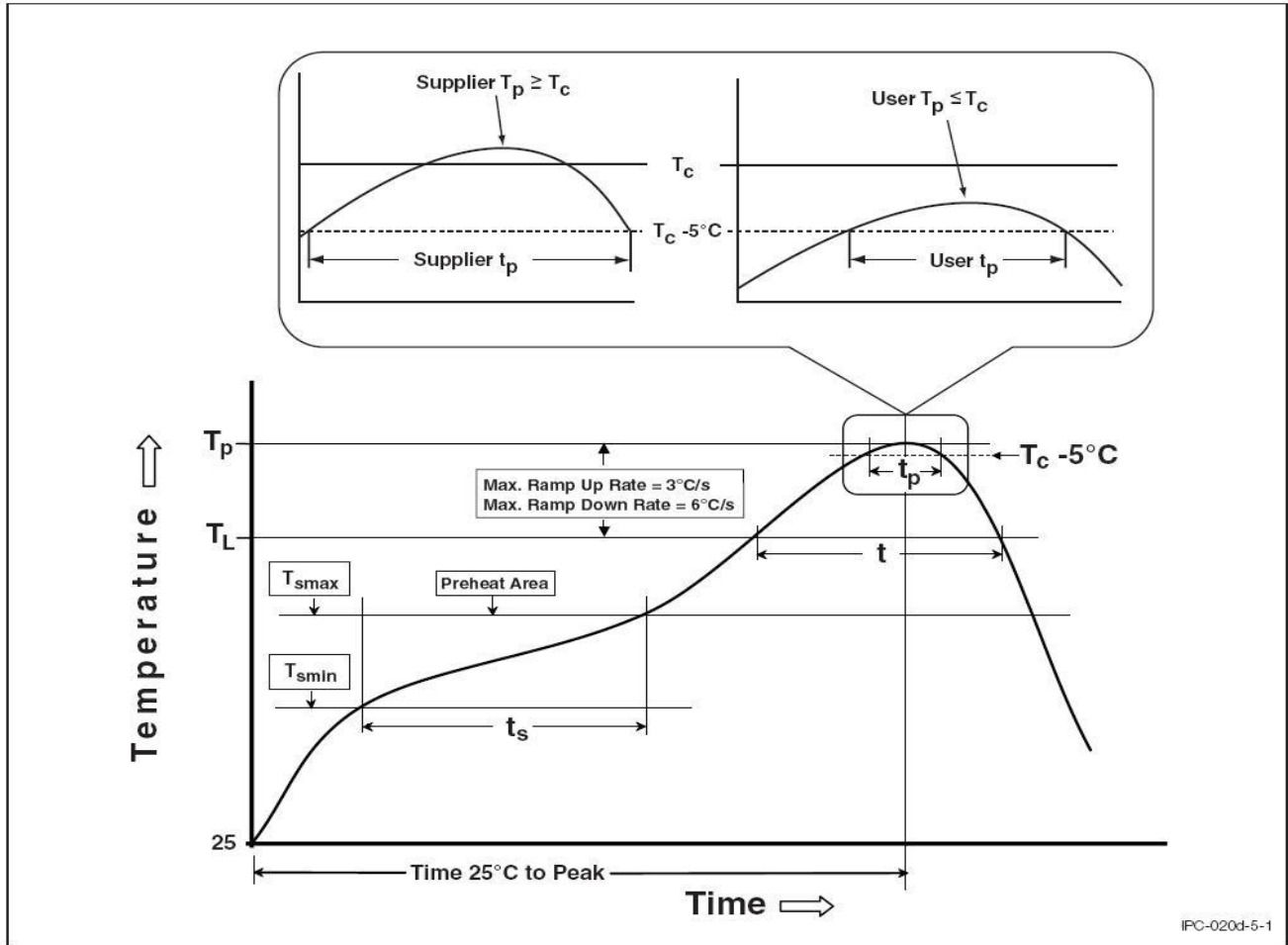


**PACKING QUANTITY**

Option	Quantity	Quantity – Inner box	Quantity – Outer box
None	45 Units/Tube	32 Tubes/Inner box	10 Inner box/Outer box = 14.4k Units
M	40 Units/Tube	30 Tubes/Inner box	10 Inner box/Outer box = 12k Units
S(T1)	1000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 15k Units
S(T2)	1000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 15k Units
SL(T1)	1000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 15k Units
SL(T2)	1000 Units/Reel	3 Reels/Inner box	5 Inner box/Outer box = 15k Units

**REFLOW INFORMATION**

**REFLOW PROFILE**



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (T <sub>sm</sub> )	100	150°C
Temperature Max. (T <sub>sm</sub> )	150	200°C
Time (t <sub>s</sub> ) from (T <sub>sm</sub> to T <sub>sm</sub> )	60-120 seconds	60-120 seconds
Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	183°C	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.



**DISCLAIMER**

- JIEJIE is continually improving the quality, reliability, function and design. JIEJIE reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
- JIEJIE makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, JIEJIE disclaims (a) any and all liability arising out of the application or use of any product, (b) any and all liability, including without limitation special, consequential or incidental damages, and (c) any and all implied warranties, including warranties of fitness for particular
- The products shown in this publication are designed for the general use in electronic applications such as office automation, equipment, communications devices, audio/visual equipment, electrical application and instrumentation purpose, non-infringement and merchantability.
- This product is not intended to be used for military, aircraft, automotive, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact JIEJIE sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify JIEJIE's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.